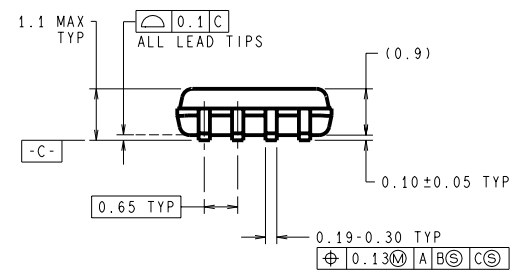
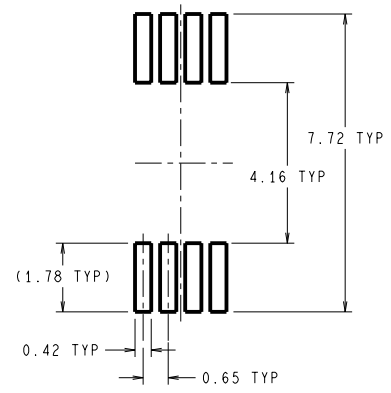
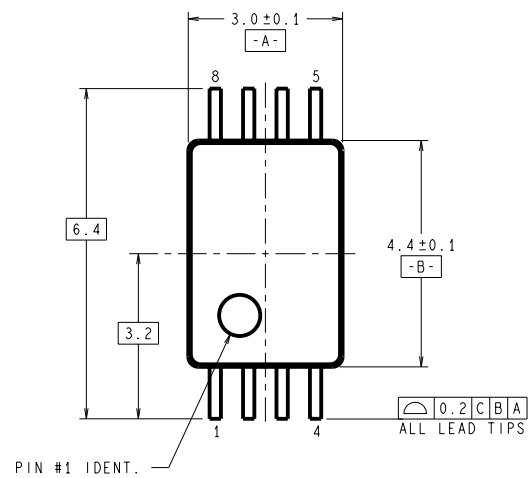
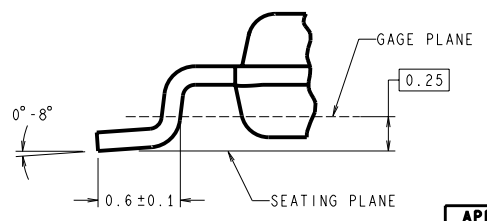
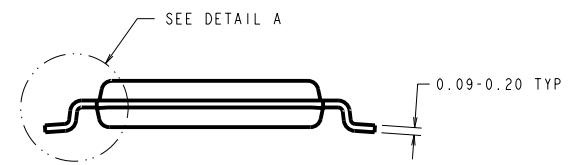


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	11077	09/01/95	MS/



DIMENSIONS ARE IN MILLIMETERS



DETAIL A
TYPICAL, SCALE: 40X

NOTES: UNLESS OTHERWISE SPECIFIED
1. REFERENCE JEDEC REGISTRATION MO-153, VARIATION AA, DATED 7/93.

APPROVALS		DATE	National Semiconductor		
DRAWN	MARTA SUCHY	09/01/95	2900 Semiconductor dr., Santa Clara, CA 95052-8090		
DFTG. CHK.			MOLDED TSSOP, JEDEC, 4.4mm BODY WIDTH, 8 LD, 0.65mm PITCH		
ENGR. CHK.					
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	MKT-MTC08	A
DO NOT SCALE DRAWING SHEET 1 of 1					